

FEATURES

- 500 MHz to 50 GHz
- Low-Profile Package
- Input Regulators for Improved Stability
 Versus Power Supply Variations
- 16 Bit Tuning Resolution

YIG TUNED BANDPASS FILTERS WITH COMMERCIAL SERIAL DRIVERS SM SERIES



DESCRIPTION

MICRO LAMBDA YIG Bandpass Filters, model types MLFP series are available with integrated serial driver circuits.

MICRO LAMBDA drivers eliminate the need for customers to design or develop their own driver circuits and sophisticated test and alignment procedures. Integrating a driver at MICRO LAMBDA's factory ensures that peak performance will be achieved at the time of manufacture. Alignment and compensation with the particular YIG filter can be maximized down to the component level.

All drivers in this series provide input voltage regulators, and compensation circuits to improve frequency drift.

COMMERCIAL SERIAL DRIVERS							
DRIVER INPUT & RESPONSE	SPECIFICATION (0 to +65 deg. C)						
Tuning Command	Start Word (all 0's) = Lowest Frequency Stop Word (all 1's) = Highest Frequency						
Tuning Resolution	16 BIT Positive Logic (Fmax-Fmin)/65,535 Bit Resolution						
Tuning Accuracy (excluding hysteresis)	See Table						
Tuning Speed	5 mS for 1 GHz step to within \pm 10 MHz.						
Main Driver Inputs Supply Voltage & Current (P1-6) (P1-5) Supply Voltage Pushing Supply Voltage Ripple Ground (P1-4, 12) YIG Heater Voltage & Current (P1-7, 8)	+15 V ± .5 V @ Filter Tuning Current +50 mA, Max. -15 V ± .5 V @ 50 mA ± 100 kHz, Max. @ ± .5 Vdc 10 mV Ripple Pk-Pk from 2 kHz to 3 MHz Chassis Ground +24 Vdc ±4 Vdc @ 300 - 750 mA surge for 2 seconds, 100 - 150 mA steady state depending on filter type. Polarity independent : ±12 Vdc or ±15 Vdc acceptable						
Digital Interface (P1-1, 2, 3, 4)	The MLWI digital driver interface is a standard 3-wire connection compat- able with SPI/QSPI/MICROWIRE interfaces. The 3-wire serial interface will operate in a 5V or 3.3V logic system. The chip-select input (SELECTn) frames the serial data loading at the data input pin (DATA). Immediately following SELECTn's high-to-low transition, the data is shift- ed synchronously and latched into the input register on the rising edge of the serial-clock input (CLOCK). After 16 data bits have been loaded into the serial input register, it transfers its contents to the DAC latch on SE- LECTn's low-to-high transition (Figure 2). Note that if SELECTn does not remain low during the entire 16 CLOCK cycles, data will be corrupted. In this case, reload the DAC latch with a new 16-bit word.						

SM-SERIES — CONT.

YIG Tuned Bandpass Filters with Commercial Serial Drivers

Power-On Reset

The MLWI digital driver has a power-on reset circuit to set the DAC's output to OV(F-min) in unipolar mode when VDD is first applied. This ensures that unwanted DAC output voltages will not occur immediately following a system power-up, such as after power loss.

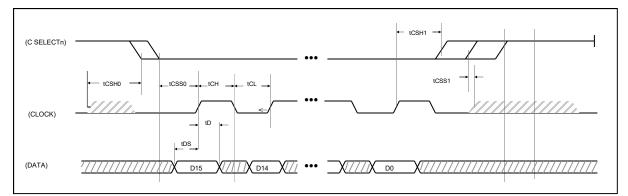
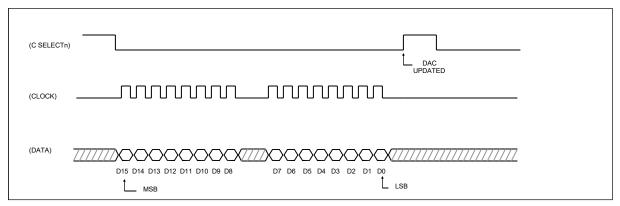
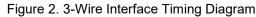


Figure 1. Timing Diagram





TIMING CHARACTERISTICS

PARAMETER	SYMBOL	CONDITIONS	MIN TYP MAX	UNITS
CLOCK Frequency	fCLK		10	MHz
CLOCK Pulse Width High	tCH		45	ns
CLOCK Pulse Width Low	tCL		45	ns
CSn Low to CLOCK High Setup	tCSS0		45	ns
CSn High to CLOCK High Setup	tCSS1		45	ns
CLOCK High to CSn Low Hold	tCSH0		30	ns
CLOCK High to CSn High Hold	tCSH1		45	ns
DATA to CLOCK High Setup	tDS		40	ns
DATA to CLOCK High Hold	tDH		0	ns
VDD High to CSn Low (power-up delay)			20	μs

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Bandpass Filters with Positive Input Serial Drivers (0° C to +65° C)

MODEL	#	Frequency	3 dB	Accuracy	Current	Current	Outline
NUMBER	Stages	GHz	Bandwidth (MHz)	(MHz) *	+15V (mA)	-15V (mA)	Drawing
MLFP-20520SM	2	.50 to 2.0	20	+/- 10	350	50	99-0021-171
MLFP-22018SM	2	2.0 to 18.0	25	+/- 20	1050	50	99-0021-171
MLFP-22026SM	2	2.0 to 26.5	20	+/- 35	1200	50	99-0021-172
MLFP-40520SM	4	.50 to 2.0	20	+/- 10	350	50	99-0021-171
MLFP-42008SM	4	2.0 to 8.0	20	+/- 20	550	50	99-0021-171
MLFP-42018SM	4	2.0 to 18.0	40	+/- 20	1050	50	99-0021-171
MLFP-42026SM	4	2.0 to 26.5	25	+/- 35	1200	50	99-0021-172
MLFP-46018SM	4	6.0 to 18.0	100	+/- 20	1050	50	99-0021-171
MLFP-48018SM	4	8.0 to 18.0	400	+/- 25	1050	50	99-0021-171
MLFP-43040SM	4	3.0 to 40.0	30	+/- 50	1450	50	99-0021-180
MLFP-43044SM	4	3.0 to 44.0	30	+/- 60	1550	50	99-0021-180
MLFP-43050SM	4	3.0 to 50.0	30	+/- 90	2100	50	99-0021-173
MLFP-47040SM	4	7.0 to 40.0	35	+/- 50	1450	50	99-0021-180
MLFP-41840SM	4	18.0 to 40.0	50	+/- 50	1450	50	99-0021-180
MLFP-60520SM	6	0.5 to 2.0	20	+/-20	350	50	99-0021-179
MLFP-62008SM	6	2.0 to 8.0	50	+/-20	550	50	99-0021-179
MLFP-62018SM	6	2.0 to 18.0	50	+/- 20	1050	50	99-0021-179
MLFP-62026SM	6	2.0 to 26.5	30	+/- 35	1350	50	99-0021-181
MLFP-66018SM	6	6.0 to 18.0	100	+/- 20	1050	50	99-0021-179
MLFP-68018SM	6	8.0 to 18.0	500	+/- 25	1050	50	99-0021-179
MLFP-70520SM	7	0.5 to 2.0	20	+/-10	350	50	99-0021-179
MLFP-72008SM	7	2.0 to 8.0	50	+/-20	550	50	99-0021-179
MLFP-72018SM	7	2.0 to 18.0	40	+/- 35	1050	50	99-0021-179
MLFP-76018SM	7	6.0 to 18.0	500	+/- 45	1050	50	99-0021-179
MLFP-78020SM	7	8.0 to 20.0	500	+/- 45	1150	50	99-0021-179
MLFP-72026SM	7	2.0 to 26.5	30	+/- 35	1350	50	99-0021-181
MLFP-76018LSM	7-L	6.0 to 18.0	500	+/- 45	1050	50	99-0021-179
MLFP-78018LSM	7-L	8.0 to 18.0	500	+/- 45	1050	50	99-0021-179
MLFP-78020LSM	7-L	8.0 to 20.0	500	+/- 45	1150	50	99-0021-179

* Accuracy includes frequency drift and linearity errors over the temperature range.

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